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*Vanhoof, B.; Nachtergaele, L.; Lafruit, G.; Peon, M.; Masschelein, B.; Catthoo Bormans, J.; Bolsens, I.;*

 Circuits and Systems for Video Technology, IEEE Transactions on , Volume: 13 , Issue: 4 , April 2003  
 Pages:348 - 357

[\[Abstract\]](#)    [\[PDF Full-Text \(998 KB\)\]](#)    IEEE JNL

**2 0.13-μm 32-Mb/64-Mb embedded DRAM core with high efficient redundancy and enhanced testability**
*Kikukawa, H.; Tomishima, S.; Tsuji, T.; Kawasaki, T.; Sakamoto, S.; Ishikawa, Abe, W.; Tanizaki, H.; Kato, H.; Uchikoba, T.; Inokuchi, T.; Senoh, M.; Fukus, Y.; Nirro, M.; Maruta, M.; Shibayama, A.; Ooishi, T.; Takahashi, K.; Hidaka, H.*  
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*Zucker, D.F.; Lee, R.B.; Flynn, M.J.;*

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*Kuo, P.-J.; Aoki, T.; Yasuda, H.;*

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**16 System-level power exploration for MPEG-2 decoder on embedded cores: a systematic approach**
*Moolenaar, D.; Nachtergaele, L.; Catthoor, F.; De Man, H.;*

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**20 Traffic analysis for on-chip networks design of multimedia applications**

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**21 Watermarking for self-authentication of compressed video**

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**23 Memory hierarchy exploration for low power architectures in embedded multimedia applications**

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**25 Implementation of a scalable MPEG-4 wavelet-based visual texture compression system**

*Nachtergaele, L.; Vanhoof, B.; Peon, M.; Lafruit, G.; Bormans, J.; Bolsens, I.;*  
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26 **Data-path synthesis of VLIW video signal processors**

*Zhao Wu; Wolf, W.;*

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